

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#9/ Electron 1/30/03

In re Patent Application of: VINSON ET AL.

erial No. 09/915,762

Viling Date: July 26, 2001

DECOUPLING CAPACITOR CLOSELY COUPLED WITH INTEGRATED

CIRCUIT

Examiner: T. Dinh

) Art Unit: 2827

SECOND RESPONSE TO REQUIREMENT FOR RESTRICTION

Director, U.S. Patent and Trademark Office Washington, DC 20231

Sir:

Applicants filed an initial Response to Rest (For Nequirement of October 1, 2002, electing Group I, Claim 1-31, drawn to an integrated chip module and multi-chip module, classified in class 361, subclass 782.

On December 31, 2002, the Examiner mailed a detailed action requesting that Applicants supply a correction by electing a single disclosed species for prosecution on the merits, i.e., either Specie I relative to FIG. 4: without a thin film metallization layer positioned on the capacitor carrier; or Specie II relative to FIG. 4A: with a thin film metallization layer positioned on the capacitor carrier.

Applicants efect Species I directed to FIG. 4: without a thin film metallization layer positioned on the capacitor carrier.

The Patent Application of: VINSON ET AL. Serial No. 09/915,762 Filing Date: July 26, 2001

If the Examiner has any questions concerning this response, the undersigned attorney would appreciate a telephone call.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE, WASHINGTON, DC 20231, on this 5th day of January, 2003.